

# C.U.SHAH UNIVERSITY

## Winter Examination-2015

**Subject Name: Electronic Devices and Circuits**

**Subject Code: 5SC01PHC4**

**Branch: M.Sc. (Physics)**

**Semester: 1    Date: 07/12/2015    Time: 10:30 To 1:30**

**Marks: 70**

**Instructions:**

- (1) Use of Programmable calculator and any other electronic instrument is prohibited.
  - (2) Instructions written on main answer book are strictly to be obeyed.
  - (3) Draw neat diagrams and figures (if necessary) at right places.
  - (4) Assume suitable data if needed.
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### SECTION – I

- |            |  |             |
|------------|--|-------------|
| <b>Q-1</b> | <b>Attempt the Following questions</b>   | <b>(07)</b> |
|            | a. Define reverse saturation current.  | <b>01</b>   |
|            | b. Define maximum symmetrical swing.   | <b>02</b>   |
|            | c. Draw symbol and V-I characteristic of Zener diode.  | <b>01</b>   |
|            | d. Gives the types of transistor with its symbol.  | <b>01</b>   |
|            | e. Define contact potential.   | <b>02</b>   |
| <b>Q-2</b> | <b>Attempt all questions</b>   | <b>(14)</b> |
|            | a) Explain diode capacitance in details.   | <b>07</b>   |
|            | b) Explain current flow mechanism in transistor.   | <b>04</b>   |
|            | c) Explain reverse recovery time.  | <b>03</b>   |
| <b>OR</b>  |  |             |
| <b>Q-2</b> | <b>Attempt all questions</b>   | <b>(14)</b> |
|            | a) Write a short note on zener diode voltage regulator.  | <b>05</b>   |
|            | b) Explain common emitter amplifier circuit analysis.  | <b>06</b>   |
|            | c) Derive an expression for current gain.  | <b>03</b>   |
| <b>Q-3</b> | <b>Attempt all questions</b>   | <b>(14)</b> |
|            | a) Explain in detail Ebers-moll transistor model   | <b>07</b>   |
|            | b) What is contact potential? Explain contact potential diode current equations under forward and reverse bias conditions. | <b>07</b>   |
| <b>OR</b>  |  |             |
| <b>Q-3</b> | <b>Attempt all questions</b>   | <b>(14)</b> |
|            | a) Explain common emitter configuration in details.  | <b>07</b>   |
|            | b) Explain breakdown mechanism in P-N Junction diode.  | <b>07</b>   |



## SECTION – II

- Q-4**      **Attempt the Following questions**      **(07)**
- a. Define pinch off voltage of JFET.      **02**
  - b. What is the principle of solar cell?      **01**
  - c. What is the full form of SCR?      **01**
  - d. What is DIAC?      **01**
  - e. What is photo conductivity?      **02**
- Q-5**      **Attempt all questions**      **(14)**
- a) Write a short note on tunnel diode.      **05**
  - b) Write a short note on Diac.      **05**
  - c) What are JFET parameters? Describe.      **04**
- OR**
- Q-5**      **Attempt all questions**      **(14)**
- a) Explain construction, and characteristics of light emitting diode.      **05**
  - b) Explain construction and characteristics of Thermistor.      **05**
  - c) What is UJT? Draw its V-I Characteristics.      **04**
- Q-6**      **Attempt all questions**      **(14)**
- a) Explain construction, Operation and characteristics of SCR.      **07**
  - b) Explain the working of a solar cell. Define its efficiency, Fill factor, short circuit current, and open circuit voltage.      **07**
- OR**
- Q-6**      **Attempt all Questions**      **(14)**
- a) Explain in detail depletion type MOSFET with construction, Operation and its V-I Characteristics.      **07**
  - b) Write a technical note on TRIAC.      **07**

